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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	Unknown 10/782,446
Filing Date	Even Date Herewith 2/19/04
First Named Inventor	Barrett, Keith
Group Art Unit	Unknown 2187
Examiner Name	Unknown Nguyen, Hiep

Sheet 1 of 1

Attorney Docket No: 303.882US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
HJ	US-5,995,405	11/30/1999	Trick, Steven G.	365	63	10/29/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
HJ		"Appendix D: DDR Synchronous DRAM (DDR SDRAM)", JEDEC Standard No. 21-2, Release 11, (Unknown), 41.2.4-1 - 4.1.2.4 -36	
HJ		YIP, GARY, et al., "Rambus SPD Assembler Revision 1.0", Rambus SPD Assembler Worksheet Revision 1.3, TL-0001-130, (Sept. 2002), 13 pgs.	

EXAMINER

Hiep T. Nguyen

DATE CONSIDERED

6/21/06

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citations in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached